Course: EE 412 ELECTRONIC INSTRUMENTATION DESIGN

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Prerequisites: EE316- Electronics 2 or equivalent or consent of the instructor.

Text: Design with Operational Amplifiers and Analog Integrated Circuits,

McGraw Hill, S. Franco, 2002, ISBN 0-07-112173-0

Other References: Microelectronic Circuits, Sedra/Smith

Cadence P-spice will be used in class designs.

Grading: Homework ......25%

Midterm 1 ......25%

Lab work ......25%

Final Examination ......25%

## **Class Objectives**

To produce graduates with the ability to design the anlog circuits utilized in electronic instrumentation.

## **Course Outcomes**

At the conclusion of EE 412, students will:

- Be able to describe and model different electrical transducers
- Design an optimum amplifier for a transducer
- Design signal conditioning circuits for limiting, filtering, and waveform shaping
- Specify the performance required from A/D and D/A converters in a design.

## Class Outline (subject to change)

- Course introduction
- Operational amplifiers
  - Specifications, evaluation
  - Limitations offset voltage and current, drift, noise, large and small signal response, distortion
  - Power supply and physical layout considerations
  - Linear Circuits- Transfer functions, integrators, differentiators, transimpedance amplifiers
  - o Nonlinear Circuits- Limiters, Clampers, Wave shaping, Log amplifiers
- Filtering practical lowpass, bandpass, bandreject, hi-pass active filter designs
  - o 2<sup>nd</sup> and 3<sup>rd</sup> order active filters
  - o Implementation of higher order filters- method of coefficients
- Noise in amplifiers
  - Estimation
  - Effects on measurement accuracy
- Transducers models and characteristics
- A/D and D/A specifications, characteristics, implementation
- Time permitting Oscillators, Phase-Lock-Loops, Modulation