step  $\Delta z$  for each simulation of the phase-matched case. This is measured on an average 600 MHz Pentium III PC, which means that the entire simulation takes about 25 min to complete.

## 4. CONCLUSION

A new technique for modeling pulsed optical beams in dielectric waveguides containing second-order nonlinear effects has been proposed and implemented. The technique is called the time-domain beam propagation method for second-harmonic generation (TD-BPM-SHG), which is an extension of the linear TD-BPM. It involves solving the coupled parabolic nonlinear equations using the explicit finite-difference technique, where all spatial and time variations were retained in the analysis. The technique was also applied to model pulsed optical beams in a dielectric waveguide containing secondorder nonlinear effects. It is concluded that the method is very efficient and simple to implement. The TD-BPM-SHG is well suited for the study of the unidirectional propagation of compact temporal pulses over long distances in a guidedwave environment. In addition, the method should find application in the study of spatio-temporal optical solitons in media with quadratic nonlinearity [17].

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# THE EFFECT OF GROUND VIAS ON CHANGING SIGNAL LAYERS IN A MULTILAYERED PCB

## Brock J. LaMeres<sup>1</sup> and T. S. Kalkur<sup>1</sup>

<sup>1</sup> Department of Electrical and Computer Engineering University of Colorado Colorado Springs, Colorado 80933-7150

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**ABSTRACT:** This paper describes the effect of ground vias on signal integrity when a signal changes layers and passes through multiple ground planes in a printed circuit board. An equivalent circuit is given that models the return current path when a signal passes through multiple ground planes that are not connected nearby the signal via. The effect is seen as a region of higher impedance than expected on the destination signal layer. Time-domain reflectometry measurements are taken on an actual printed circuit board that exhibits this behavior. The empirical measurements are compared to SPICE simulations of the equivalent circuit to verify the accuracy of the model. © 2001 John Wiley & Sons, Inc. Microwave Opt Technol Lett 28: 257–260, 2001.

Key words: multilayered circuits; packaging; interconnects; PC boards

# I. INTRODUCTION

In a multilayered printed circuit board (PCB), microstrip and strip-line transmissions lines frequently need to be able to change signal layers [1]. In most cases, this involves passing through multiple ground planes. When this occurs, the ground return current must also change the layer upon which it returns to the source driving the signal. If the return current does not maintain a constant physical relationship to the signal, the signal may become distorted when it reaches the signal layer to which it is changing. The negative impact of this phenomenon can be avoided by "tacking" the ground planes of a PCB together with vias in the regions where the transmission lines change signal layers. By connecting the ground planes together near the signal vias, the return current is able to maintain its relationship to the forward traveling signal and avoid distortion. Figure 1 shows a cross section



Figure 1 Cross section of a signal changing layers in a PCB

of a signal changing layers on a printed circuit board while passing through two ground planes.

In the literature, most of the research was confined to a single ground via in a PCB [2-4]. This paper investigates the effect of ground vias in a printed circuit board. This is accomplished by taking time-domain reflectometry (TDR) measurements on a printed circuit board trace that passes through multiple ground planes that do not have ground via connections near the signal via. An equivalent circuit is then developed to show the effect of not connecting the ground planes together near the signal via. This effect is modeled by including an inductance in the ground return path of the transmission line on the destination signal layer. The ground planes of the PCB are then tacked together using pseudoground vias, and the TDR measurements are repeated. The measurements will show that, by tacking the ground planes together near the signal via, the negative effect of an impeded return current can be avoided. The next section gives the theoretical background for this investigation.

# **II. THEORY**

When a step pulse propagates down a transmission line on a printed circuit board, the wavefront sequentially charges up every point on the line to the final steady-state voltage of that signal. When a point on the transmission line is charged, an equal and opposite charge is developed in the ground plane to which the signal is referenced. In high-speed transport systems, the time it takes to charge the line is of great importance to the overall speed of the system. One of the factors that limits the speed at which a point on the line can be charged is the time it takes for the equal and opposite charge in the ground plane to be developed. The development of this equal and opposite charge in the ground plane, also known as the return current, can be slowed when a signal changes layers in a PCB. Figure 2 shows the return current charge as a step pulse propagates down a transmission line above a ground plane.

When a signal changes layers in a printed circuit board and passes through multiple ground planes, the signal voltage is referenced to a new ground plane. There is a finite amount of time for the equal and opposite charge to be developed in



Figure 2 Step pulse traveling above a ground plane

the new ground plane. If the ground planes are connected together with vias in the region where the signal changes layers, the amount of time it takes for the equal and opposite charge to be developed is negligible. This is because the charge can be drawn from the point immediately preceding the rising edge of the pulse through the ground vias. If the ground planes are *not* connected together near the region where the signal changes layers, the equal and opposite charge must be drawn from a point further away. The time it takes for the charge to be developed may not be negligible, and could cause the rise time of the pulse's edge to be degraded.

This phenomenon can also be described by saying that the trace on the layer to which the signal is changing has a *higher* initial impedance than that of the original layer. The higher impedance of the destination transmission line can be modeled by including an inductance in the ground current return path of the transmission-line model. This is an accurate representation of the actual response of the system because the model shows a finite time delay in developing the appropriate negative charge in the ground plane. Figure 3 shows the TDR experimental setup and equivalent circuit of a PCB trace changing signal layers through multiple ground planes without sufficient ground-plane connections near the signal via. The parasitic effect of the signal via is modeled as a discrete capacitance.

The series inductance of the destination signal layer is increased due to the inclusion of the inductance in the return current path (L''). Equation (1) [5] gives the definition of characteristic impedance for a lossless transmission line. This equation shows that the characteristic impedance is proportional to the inductance of the line, and that an increase in inductance will result in an increase in impedance.

$$Z_0 = \sqrt{\frac{L'}{C'}} \tag{1}$$

where L' = inductance per unit length and C' = capacitance per unit length.

#### **III. EMPIRICAL DATA**

This section shows the time-domain reflectometry measurements made on a printed circuit board microstrip transmission line that changes signal layers and passes through multiple ground planes. Figure 4 shows the test PCB on which the measurements were taken.

Initially, the ground planes are only connected together around the edges of the board. When the signal trace changes layers in the middle of the board, there is a finite amount of



**Figure 3** Equivalent circuit of a transmission line changing layers on a PCB (without ground-plane connections near the signal via)



Figure 4 Test printed circuit board

time for the equal and opposite charge to be developed in the new ground plane to which the signal is referenced. This effect will make the destination transmission line appear to have a higher initial impedance than the original PCB trace when performing TDR measurements. Figure 5 shows the results of the TDR measurement.

The original trace has a characteristic impedance of 75  $\Omega$ . After changing layers using the signal via (shown as a capacitive discontinuity), the signal encounters an impedance greater than 75  $\Omega$ . This is due to not having nearby connections between the multiple ground planes. In this measurement, the following electrical parameters were extracted:

 $Z_0$  of the original PCB trace = 75  $\Omega$ 

C of the signal via = 375 pF

 $Z'_0$  of the destination PCB trace = 77  $\Omega$ 

L'', excess inductance in return current path = 400 pH.

The next TDR measurement was taken on the same trace after the ground planes were connected together near the signal via using pseudoground vias. This was accomplished by drilling holes into the PCB, and then inserting metal pins to mimic ground via connections. Figure 6 shows the TDR measurement of the same trace after the connection of the ground planes. Also shown is the TDR measurement of the trace before the ground vias were in place.

This clearly shows that connecting the ground planes together near the signal via reduces the negative effect of a delayed return current.

The equivalent circuit of the printed circuit board trace changing layers without the proper connection of ground planes (Fig. 3) was simulated to verify its functionality. The same circuit, without including the inductance present in the ground return path of the destination trace, was simulated to show the *ideal* case. Theoretically, the TDR measurements taken on the PCB trace after the pseudoground vias were in place should match the ideal case. Figure 7 shows the simula-



Figure 5 TDR measurements on a trace without proper ground connections



Figure 6 TDR measurements on a trace after proper ground-plane connections



Figure 7 Equivalent circuit simulation results

tion results of the equivalent circuits for both the *pre*- and *postconnection* of ground planes near the signal via. Also shown are in the empirical TDR data for both cases.

This figure shows that the simulation data closely match the empirical data for both cases. This indicates that including an inductance in the ground return path of the destination PCB trace in the equivalent circuit is an accurate model for an *impeded* return current path.

## **IV. CONCLUSION**

This paper investigated the effect of return current on the signal integrity of a signal changing layers on a printed circuit board passing through multiple ground planes without a proper connection between the ground planes. Time-domain reflectometry measurements showed that, by connecting the ground planes together with vias in the region where a signal changes layers, the negative effects of an impeded return current can be avoided. An equivalent circuit was also presented to model the effect of a return current that has a finite time delay in developing the necessary negative charge in the ground plane to support the incident signal. By including an inductance in the ground return current path of the transmission-line model of a PCB trace, the behavior of the impeded return current can be accurately modeled. The simulation results were compared to the empirical data to verify the equivalent circuit's accuracy.

When designing a high-speed transport system using printed circuit boards, signal integrity is of great importance to the overall speed of that system. Any distortion added to a signal can lead to slower rise times and a reduction of performance for that system. This paper has shown that distortion added by an *impeded* ground return current due to passing through multiple ground planes in a PCB can be avoided by including vias that connect the multiple ground planes together near the region where the signal changes layers.

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# ORTHOGONAL CIRCULAR POLARIZATION AXICON BEAM SPLITTER

#### Mansour K. Mansour<sup>1</sup> and Mohammed A. Habli<sup>2</sup>

 <sup>1</sup> Department of Electronics Engineering Higher Colleges of Technology AlAin, United Arab Emirates
 <sup>2</sup> Department of Electrical Engineering United Arab Emirates University AlAin, United Arab Emirates

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ABSTRACT: The design of an orthogonal circular polarization axicon beam splitter (OCPABS) that generates orthogonal circular polarization states (OCPS) is presented. The OCPABS consists of an absorbing V-shaped substrate coated with a transparent single-layer thin film. The power reflectances range between 25.19 and 56.24% in the infrared (IR), visible, and ultraviolet (UV) light regions. © 2001 John Wiley & Sons, Inc. Microwave Opt Technol Lett 28: 260–265, 2001.

**Key words:** orthogonal circular polarization states; beam splitter; IR to UV light regions; single-layer coating; absorbing substrate

#### 1. INTRODUCTION

Recently, we have introduced a reflector axicon polarization beam splitter (APBS) that operates in the UV range and a polarization beam splitter for visible light (PBS–VL) [1–2]. Both devices consist of an absorbing V-shaped substrate coated with thin-film layers. In this paper, we introduce the orthogonal circular polarization axicon beam splitter (OC-PABS). The OCPABS is a device that can generate orthogonal circular polarization states (OCPS). Azzam [3] generated spatial OCPS in the IR and visible region ranges with power reflectances that are less than or equal to 33.35%. The OCPABS presented in this paper operates in a wide spectrum range, namely, the IR, visible, and UV light regions with power reflectances up to 56.24%.

#### 2. PROBLEM DESCRIPTION

Consider the reflective polarization beam splitter structure shown in Figure 1. The structure consists of two planes, the + plane and the - plane that make angles  $\theta_+$  and  $\theta_-$  with

the horizontal plane, respectively. The + plane reflects the right-handed circular polarization state, while the - plane reflects the left-handed circular polarization state. A plane wave of monochromatic light traveling in a transparent medium of refractive index  $N_0$  is incident at an angle  $\phi_{0+}$ and at an angle  $\phi_{0-}$  on the + and - planes, respectively. The incident light is linearly polarized at 45° azimuth with respect to the plane of incidence. Both surfaces are made of an absorbing substrate of complex refractive index  $N_{2\nu} = n_{2\nu}$  $-jk_{2\nu}$  coated by a transparent thin film of refractive index  $N_{1\nu}$ . The subscript  $\nu$  is used to refer to the thin film on the + and – surfaces. The film has a uniform thickness  $d_{\nu}$  over the corresponding total area of the substrate. The media of incidence, film, and substrate, denoted by 0, 1, and 2, respectively, over both planes, are assumed to be linear, homogeneous, isotropic, and nonmagnetic, and are separated by parallel-plane interfaces.

The complex amplitude reflection coefficient of the film-substrate system is given by [4]

$$R_{v/v} = (r_{01v/v} + r_{12v/v}X_v)/(1 + r_{01v/v}r_{12v/v}X_v) \quad (1)$$

where v is used to indicate p and s polarizations, v is used to indicate the + and - planes, where

$$X_{\nu} = e^{-j2\pi\zeta_{\nu}} \tag{2}$$

$$\zeta_{\nu} = d_{\nu} / D_{\phi_{0\nu}} \tag{3}$$

$$D_{\phi_{\nu}} = \frac{\lambda}{2} \left( N_{1\nu}^2 - N_0^2 \sin^2 \phi_{0\nu} \right)^{-1/2}.$$
 (4)

 $\zeta_{\nu}$  is the normalized film thickness,  $D_{\phi_{\nu}}$  is the film thickness period, and  $\lambda$  is the free-space wavelength of the light incident on the structure.  $r_{01\nu/\nu}$  and  $r_{12\nu/\nu}$  are Fresnel's reflection coefficients of the 01 and 12 interfaces of the + and - planes for the *p* and *s* polarizations given by

$$r_{01p/\nu} = \frac{\tan(\phi_{0\nu} - \phi_{1\nu})}{\tan(\phi_{0\nu} + \phi_{1\nu})} \tag{5}$$

$$r_{01s/\nu} = -\frac{\sin(\phi_{0\nu} - \phi_{1\nu})}{\sin(\phi_{0\nu} + \phi_{1\nu})} \tag{6}$$

where the indexes of refraction and the angles on both sides of the interface are related by Snell's law as

$$N_0 \sin(\phi_{0\nu}) = N_{1\nu} \sin(\phi_{1\nu}). \tag{7}$$

Similar expressions can be written for the 1–2 interface:

$$r_{12p/\nu} = \frac{\tan(\phi_{1\nu} - \phi_{2\nu})}{\tan(\phi_{1\nu} + \phi_{2\nu})} \tag{8}$$

$$r_{12s/\nu} = -\frac{\sin(\phi_{1\nu} - \phi_{2\nu})}{\sin(\phi_{1\nu} + \phi_{2\nu})}.$$
(9)

The generation of the orthogonal (right- and left-handed) circular polarization states requires that

$$\rho_{\nu} = \frac{R_{p/\nu}}{R_{s/\nu}} = \pm j \tag{10}$$

where  $j^2 = -1$ . The  $\rho_+ = +j$  and  $\rho_- = -j$  are used to generate the right-handed and left-handed circular polarization states, respectively.