EELE 250: Circuits, Devices, and Motors

Lecture 4

Assignment Reminder

- Read 2.4 2.7
- Practice problems:

P2.34, P2.36, P2.37, P2.48, P2.49, P2.53P2.67, P2.68, P2.71

• D2L Quiz #2 by 5PM on Friday. NOTE that the quiz requires equation solving, so be ready to do linear algebra!

Circuit Analysis

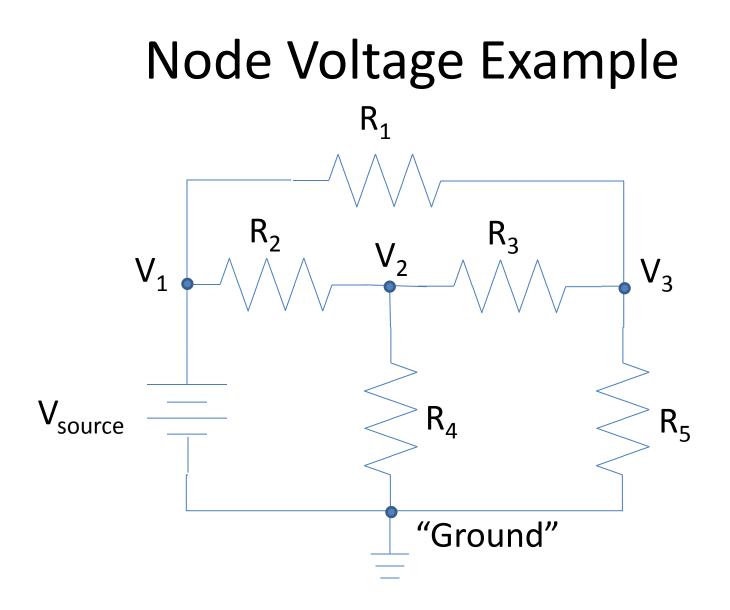
General techniques to find currents and voltages in electrical networks

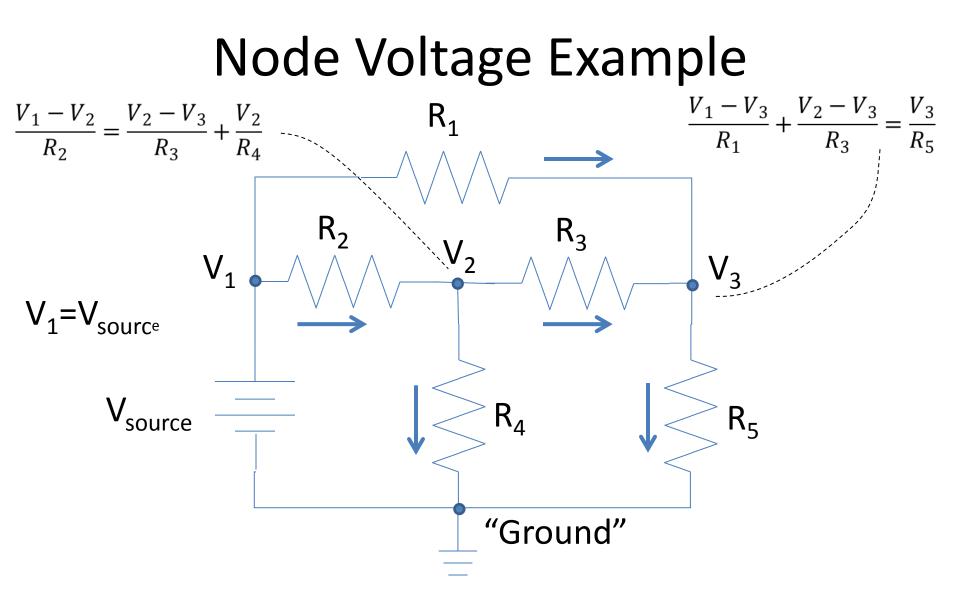
• Use KVL, KCL, and Ohm's Law

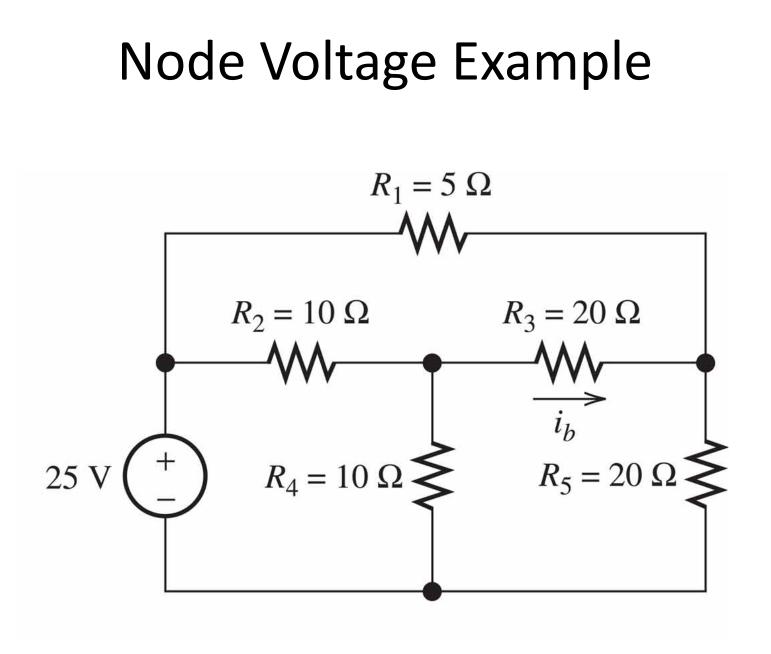
 Remember: voltages and currents can be positive or negative, so be meticulous with the math!

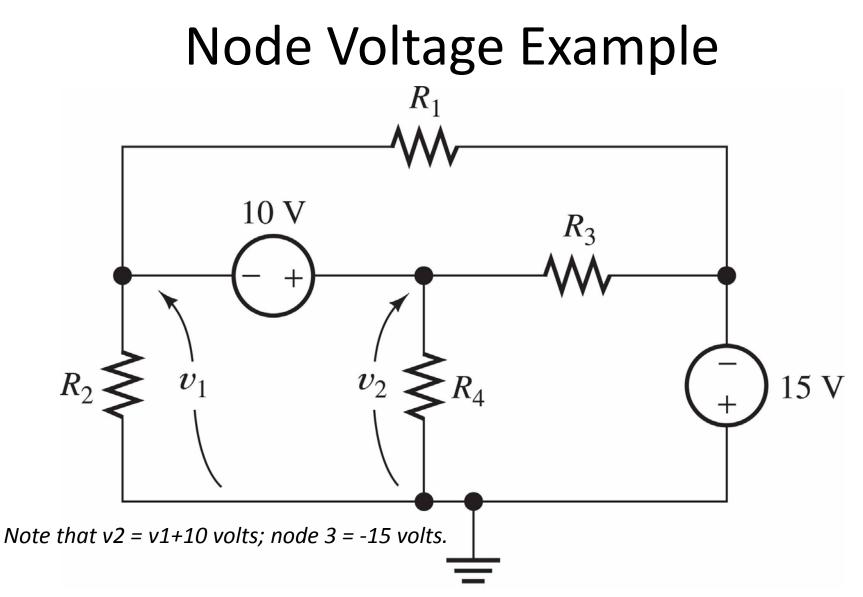
Node-Voltage Analysis

- Identify all the nodes in the circuit
- Assign one of the nodes to be the reference point, referred to as the "ground" node
- Label the other nodes as V₁, V₂, etc. These unknown voltages are with respect to the "ground" node.
- Write a KCL expression at each node, and solve for the unknown voltages









Problem: voltage source s do not constrain current, so how do we write KCL? Need to use "supernode" concept.

Summary and Review

- Nodes
- Assign reference and unknown labels
- Identify any known node voltages
- Write KCL expressions
- Solve for the unknowns
- Remember to keep track of the signs (positive and negative)

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