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INSTRUMENTS

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Windows® Hardware Engineering Conference

Modem/Audio Integration



Concurrent Audio And Modem Acceleration

**Dr. Rob Maher
Engineering Manager
AudioPCD/CABU
3Com Corporation**

Outline

- u Introduction and Scope
- u Impact of Audio/Modem Acceleration
- u Features and Cost Considerations
- u Scalable Signal Processing
- u Conclusions

Introduction

Acceleration means...

- u **Hardware optimized for function**
- u **Quality of service**
- u **Reduced impact on other system resources = better user experience**
- u **Cost for the accelerator silicon**

Product Requirements

Concurrency is essential today...

- u Modem functions (V.90, fax, voice)
- u Audio functions

...and tomorrow:

- u DSL
- u Streaming video
- u Speech recognition/synthesis
- u Advanced audio codecs

A programmable accelerator
makes this possible

User Expectations

- u Excellent value (quality/price)
- u Easy installation and widespread compatibility (fewer support calls!)
- u Demonstrably better performance and reliability
- u Upgrade potential

Features And Cost Considerations

- u Simultaneous acceleration of DirectX® audio APIs with hi-fi hardware sample rate conversion and mixing
- u V.90 data/fax/voice modem
- u Scalable signal processing to utilize host MIPS, but only when necessary
- u Cost delta vs. fully soft audio/modem and AMR: \$5 - \$15 depending on range of features

Performance Impact

(Ref: PC 98 section 17)

- u **Game audio**
 - u 24 voice Synthesizer at 22 kHz: 12 MHz
 - u 8 channel HRTF 3D at 22 kHz: 28 MHz
 - u Total: 40 MHz
- u **Soft modem**
 - u Typical designs cost 60 MHz in training, and drop to 40 MHz in transmission
- u **Total for concurrent operation: 90 Mhz, 30% of a minimum PC 99 PC**

Accelerator Economics (1 of 3)

- u **Assuming:**
 - u The previous slide is a representative load
 - u A minimum PC 99 CPU (300 MHz) costs \$63
 - u An accelerator costs about \$10
- u **Spending 30% of the CPU on concurrent 3D audio and modem costs about \$19, while in use**
- u **This analysis changes constantly, because CPU prices follow Moore's Law**
- u **The conclusions don't change, because Moore's law applies to DSPs, too**

Accelerator Economics

(2 of 3)

- u Hardware-software migration is possible for other system components and applications
- u Save the CPU MIPS for:
 - u Soft DVD
 - u Graphics intensive games
 - u Videophone codecs
 - u IP network security processing
 - u Encryption for secure commerce
 - u VPNs for secure connection from home networks to corporate networks

Accelerator Economics

(3 of 3)

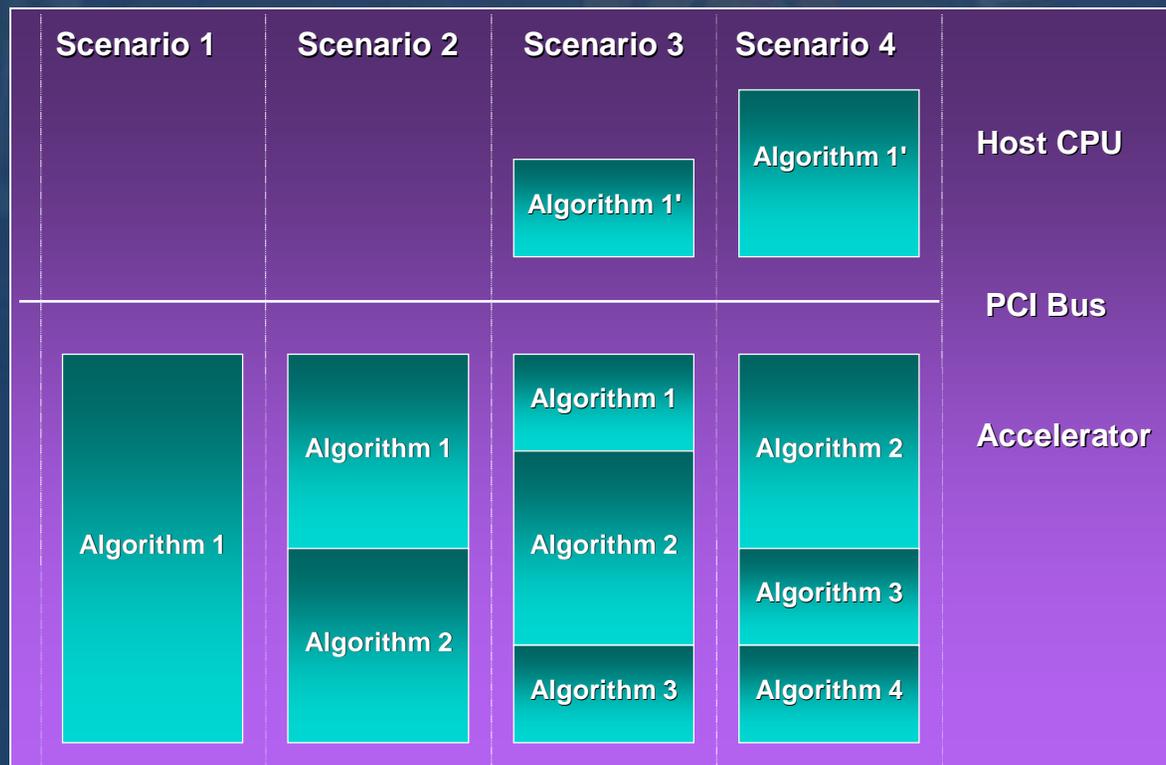
- u PC OEMs report average support call rates of 28%
- u Soft audio and modems work, but they are still vulnerable to long latency events
- u These rare events may cause support calls:
 - u If they break a modem connection
 - u If they introduce irritating audible noise
- u Soft devices present a new challenge:
 - u Will the net additional support calls cost more in dollars and customer goodwill than the saved material cost? The customers will tell us.

Scalable Signal Processing

- u Provide maximum performance and broad range of features
- u Assign priority to using accelerator resources first, then use host only as necessary
- u Algorithms can utilize all available resources to minimize “captive” costs
- u Active load monitoring allows optimal resource allocation

Fully Scalable Algorithms

- u Algorithms run on accelerator or host
- u Algorithms can scale their resource usage



Conclusions

- u Acceleration offers demonstrably increased performance and reliability
- u A modest CPU plus audio/modem accelerator can provide lower overall system cost with performance equivalent to a more expensive CPU
- u Scalable signal processing provides the best of both worlds

Call To Action

- u Understand the economic analysis of audio/modem acceleration
- u Consider the total cost of ownership (BOM, support calls, etc.) when choosing between soft and accelerated architectures
- u WDM is designed to support accelerators: Use it!

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